

[Web](#) [Images](#) [Video](#) [News](#) [Maps](#) [Gmail](#) [more ▾](#)[Sign in](#)[Google](#)

out of order execution of access requests

[Search](#)[Advanced Search](#)
[Preferences](#)New! [View and manage your web history](#)[Web](#) [Groups](#) Results 1 - 10 of about 1,940,000 for **out of order execution of access requests**. (0.29 second)**Out of order execution memory access request FIFO - Patent 6684301**

A circuit generally comprising a queue having an input and an output. The queue may be used to buffer memory **requests** generated by a processor to **access** a ...

www.freepatentsonline.com/6684301.html - 47k - [Cached](#) - [Similar pages](#)

Random access memory controller with out of order execution ...

Random **access** memory controller with **out of order execution** ... the client **request** into a plurality of simple memory **access requests** having a first **order**, ...

www.freepatentsonline.com/7093094.html - 48k - [Cached](#) - [Similar pages](#)

[[More results from www.freepatentsonline.com](#)]

Out of order execution memory access request FIFO - US Patent 6684301

Out of order execution memory access request FIFO - US Patent 6684301 from Patent Storm. A circuit generally comprising a queue having an input and an ...

www.patentstorm.us/patents/6684301.html - 14k - [Cached](#) - [Similar pages](#)

Disk controller configured to perform out of order execution of ...

Disk controller configured to perform **out of order execution** of write - US Patent ... **Access request** queuing 710/43 , Physical position 711/154 , Control ...

www.patentstorm.us/patents/6826650.html - 18k - [Cached](#) - [Similar pages](#)

[[More results from www.patentstorm.us](#)]

Out of order execution memory access request FIFO - Patent Review ...

The write FIFO temporarily stores the write **access requests** at a speed that matches the processor. The write **access requests** are stored in **order** of arrival. ...

www.wikipatents.com/6684301.html - 138k - [Cached](#) - [Similar pages](#)

Speculative pre-flush of data in an out-of-order execution ...

Typically, after the **out of order execution** of instructions, However, if the transaction is a memory **access request** resulting from a cache miss, then, ...

www.wikipatents.com/6408363.html - 156k - [Cached](#) - [Similar pages](#)

[[More results from www.wikipatents.com](#)]

(WO/2003/098392) OUT OF ORDER DRAM SEQUENCER

Title:, **OUT OF ORDER DRAM SEQUENCER**. Abstract:. Memory **access requests** are ...

After **execution**, data read from the memory device by the **execution** of the ...

www.wipo.org/pctdb/en/wo.jsp?wo=2003098392 - 14k - [Cached](#) - [Similar pages](#)

[PDF] Out-of-Order Execution of Interruptible Codes

File Format: PDF/Adobe Acrobat - [View as HTML](#)

When pipeline latencies go beyond some limit, **out-of-order execution** An external interrupt is in fact a **request** for acquiring processor resources. ...

<ftp://ftp.inria.fr/INRIA/publication/publi-pdf/RR/RR-2139.pdf> - [Similar pages](#)

[PDF] Performance impact of the L2 contention on out-of-order execution ...



File Format: PDF/Adobe Acrobat - [View as HTML](#)

memory or L2 cache **access** time. r. 1-3 cycles , the optimal size. of the cache block has been shown to be 32 or 64 bytes 18, 7. 3.2 **Out-of-order execution** ...

tab.computer.org/tcca/NEWS/mar97/seznec.pdf - [Similar pages](#)

[PDF] **Error detection and handling in a superscalar, speculative out-of ...**
drop all earlier **requests** sent by the CPU. The caches are Patkar, et. al., "A 64b 4-Issue
Out-of-Order. Execution RISC Processor," Proceedings Intl. ...
ieeexplore.ieee.org/iel2/3246/9797/00466952.pdf?arnumber=466952 - [Similar pages](#)

Google Groups results for out of order execution of access requests

-  [CORE-2007-0817: Remote Command execution, HTML and ...](#) - mailing.unix.bugtraq - Sep 26, 2007
-  [IBM Gives "FOSS" Free Access to 500 Patents](#) - comp.os.linux.advocacy - Jan 12, 2005
- [CORE-2007-0817: Remote Command ...](#) - Secure Computing - Sep 25, 2007

[1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#) **Next**

Download [Google Pack](#): free essential software for your PC

out of order execution of access req

[Search within results](#) | [Language Tools](#) | [Search Tips](#) | [Dissatisfied? Help us improve](#)

©2007 Google - [Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [About Google](#)

[Web](#) [Images](#) [Video](#) [News](#) [Maps](#) [Gmail](#) [more ▾](#)

[Sign in](#)

Google

out of order execution of access requests with

[Advanced Search](#)
[Preferences](#)

New! [View and manage your web history](#)

Web [Groups](#) Results **1 - 10** of about **1,650,000** for **out of order execution of access requests with pointers**

Disk controller configured to perform out of order execution of ...

Disk controller configured to perform **out of order execution** of write - US Patent ... **Access request queuing** 710/43 , Physical position 711/154 , Control ...

www.patentstorm.us/patents/6826650.html - 18k - [Cached](#) - [Similar pages](#)

System and method for permitting out-of-order execution of load ...

Support for **out-of-order execution** of loads and stores in a processor The **pointers** **sc_comp--** 1tag and **sc_comp_stag** for the LRQ 220 and SRQ 222, ...

www.patentstorm.us/patents/6266768-description.html - 32k - [Cached](#) - [Similar pages](#)

[[More results from www.patentstorm.us](#)]

Microinstruction pointer stack including speculative pointers for ...

The **out of order execution** core 18 includes an **out of order** One way to make the stack **pointers** viable is to allow **access** through a control register. ...

www.freepatentsonline.com/7231511.html - 53k - [Cached](#) - [Similar pages](#)

System and method for permitting out-of-order execution of load ...

An advantage of **out-of-order execution** of instructions is that it allows load The **pointers** **sc_comp.sub--** 1tag and **sc_comp_stag** for the LRQ 220 and SRQ ...

www.freepatentsonline.com/6266768.html - 36k - [Cached](#) - [Similar pages](#)

[[More results from www.freepatentsonline.com](#)]

CS530 Project

"Deep, **out-of-order**, speculative **execution engine**" (1) ... "A miss in the L2 cache typically initiates two 64-byte **access requests** to the system bus to fill ...

www.cs.utk.edu/~britt/cs530/ - 25k - [Cached](#) - [Similar pages](#)

[PDF] Memory latency-tolerance approaches for itanium processors: out-of ...

parable to **out-of-order execution** through effective instruction scheduling. However, in applications with complex memory **access patterns** and very short ...

ieeexplore.ieee.org/iel5/7816/21483/00995709.pdf?arnumber=995709 - [Similar pages](#)

[PDF] SPARC64: A 64-b 64-Active-Instruction Out- of- Order-Execution MCM ...

Committed), and four **pointers** are used to track them. The **out-of-order CPU requests** while still providing the status information for the processor to ...

[ieeexplore.ieee.org/iel1/4/10183/00475709.pdf?](http://ieeexplore.ieee.org/iel1/4/10183/00475709.pdf?tp=&isnumber=10183&arnumber=475709&type=ref)

[tp=&isnumber=10183&arnumber=475709&type=ref](http://ieeexplore.ieee.org/iel1/4/10183/00475709.pdf?tp=&isnumber=10183&arnumber=475709&type=ref) - [Similar pages](#)

[[More results from ieeexplore.ieee.org](#)]

[PDF] Cheap Out-of-Order Execution using Delayed Issue

File Format: PDF/Adobe Acrobat - [View as HTML](#)

Instructions within a queue are issued in **order**; **out of order execution** results and multiplication take 3 cycles, integer/**pointer** operations take 1 ...

www.ai.mit.edu/projects/aries/Documents/Memos/ARIES-06.pdf - [Similar pages](#)



Intel® Smart Memory Access: Minimizing Latency on Intel® Core ...

Intel® Smart Memory **Access** Improves **Execution** Throughput. Memory Disambiguation Enables Enhanced **Out-of-Order** Processing. IP-Based Prefetchers Add to Intel ...

www.intel.com/technology/magazine/computing/smart-memory-access-0906.htm - 50k - [Cached](#) - [Similar pages](#)

[PDF] **An Out-of-Order Execution Technique for Runtime Binary Translators**
code addresses, which we call virtual instruction **pointers** We've shown that the **out-of-order execution** of legacy user ...
portal.acm.org/ft_gateway.cfm?id=291039&type=pdf&coll=portal&dl=ACM - [Similar pages](#)

Google Groups results for out of order execution of access requests with pointers

-  [IBM Gives "FOSS" Free Access to 500 Patents](#) - comp.os.linux.advocacy - Jan 12, 2005
 [Documents/SchedulingInUNIXAndLinux](#) - Linux Kernel Newbies - linuxkernelnewbies - Sep 9, 2007

1 2 3 4 5 6 7 8 9 10 **Next**

Try [Google Desktop](#): search your computer as easily as you search the web.

out of order execution of access req

[Search within results](#) | [Language Tools](#) | [Search Tips](#) | [Dissatisfied?](#) [Help us improve](#)

©2007 Google - [Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [About Google](#)